

Application No. 10/773,860
Amendment dated October 31, 2005
After Final Office Action of September 13, 2005

Docket No.: 30320/18023

AMENDMENTS TO THE CLAIMS

1. (Previously presented) An article comprising a machine-accessible medium having stored thereon instructions that, when executed by a machine, cause the machine to:

measure power usage on the machine; and

in response to a measured quantum of power usage, sample state data of the machine.

2. (Currently Amended) The article of claim 1, having further instructions that, when executed by the machine, cause the machine to:

provide the sampled state data to a performance analysis module for determining a power profile based on the state data.

3. (Original) The article of claim 1, wherein the machine has a power measurement module.

4. (Original) The article of claim 3, wherein the machine comprises a plurality of subsystems and wherein the power measurement module is coupled to at least one of the plurality of subsystems for measuring power usage of the at least one of the plurality of subsystems.

5. (Original) The article of claim 4, having further instructions that when executed on the machine, cause the machine to:

measure power usage of at least one of the plurality of subsystems.

6. (Original) The article of claim 5, wherein the at least one of the plurality of subsystems includes a network subsystem, a graphics display subsystem, or a data storage subsystem.

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7. (Original) The article of claim 5, wherein the at least one of the plurality of subsystems includes an input/output device or an expansion slot subsystem.

8. (Original) The article of claim 1, wherein the state data is a program counter.

9. (Previously presented) The article of claim 1, wherein the state data comprises a program counter, status of the machine, status of at least one subsystem of the machine, status of at least one component of the machine, or status of at least one functional unit embedded in a subsystem.

10. (Previously presented) A method of profiling code executable on a machine, comprising:

measuring power usage on the machine; and

in response to a measured quantum of power usage, sampling state data on the machine.

11. (Original) The method of claim 10, wherein the machine comprises a plurality of subsystems, and wherein measuring power usage comprises measuring power delivered to at least one of the plurality of subsystems.

12. (Original) The method of claim 10, wherein the machine comprises a plurality of subsystems, measuring power usage comprising measuring power consumed by at least one of the plurality of subsystems.

13. (Original) The method of claim 10, wherein the machine comprises a plurality of subsystems and a power measurement module capable of measuring current or power delivered to at least one of the plurality of subsystems.

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14. (Original) The method of claim 13, wherein the at least one of the plurality of subsystems includes a network subsystem, a graphics display subsystem, or a data storage subsystem.

15. (Original) The method of claim 13, wherein the at least one of the plurality of subsystems includes an input device or an expansion slot device.

16. (Original) The method of claim 10, further comprising:
providing power to the machine.

17. (Original) The method of claim 10, further comprising:
providing the sampled state data to a performance analyzer.

18. (Original) The method of claim 10, wherein the state data is a program counter.

19. (Original) The method of claim 10, wherein the state data comprises a program counter, status of the machine, status of at least one subsystem of the machine, status of at least one component of the machine, or status of at least one functional unit embedded in a subsystem.

20. (Currently Amended) An apparatus comprising:
a power measurement module capable of measuring power usage in the apparatus and capable of determining when a quantum of power has been used; and

a power sampling module coupled to the power measurement module for sampling state data of the apparatus in response to a determination that the quantum of power has been used a power usage metric measured by the power measurement module.

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21. (Original) The apparatus of claim 20, further comprising a power source.

22. (Currently amended) The apparatus of claim 20, further comprising a power analysis module capable comparing the sampled state data to stored state data wherein the power sampling module usage metric is a quantum of power used on the apparatus.

23. (Original) The apparatus of claim 20, wherein the state data comprises a program counter, status of the machine, status of at least one subsystem of the machine, status of at least one component of the machine, or status of at least one functional unit embedded in a subsystem.

24. (Previously presented) The article of claim 1, having further instructions that when executed on the machine, cause the machine to profile power usage of code executing on the machine.

25. (Previously presented) The article of claim 1, wherein the machine comprises a plurality of subsystems, the article having further instructions that when executed on the machine, cause the machine to profile power usage of code executing within one of the plurality of subsystems.

26. (Previously presented) The article of claim 1, wherein the state data comprises a stack pointer, current memory usage, a number of instructions executed, or a number of accesses to a memory storage.